ABSTRACT

Two measures are provided in order to allow a serial bus system to be operated such that it is resistant to interference and defects, and such

5   that it operates with low power consumption. Firstly, each transmitted digital signal is interpreted as a logic zero or a logic one, respectively, when a first or a second predetermined current waveform, respectively, is applied to a bus line. Secondly, the bus system is operable in an energy saving mode, in which either no data transmissions at all are made, or in which

10   data transmissions are made only rarely, between the individual bus modules.